

New Proportional Integral Controller for N^{th} Order Transfer Function Model

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Abstract—The windup phenomenon in control has results in performance degradation or even system instability. The existing methods manipulate the integral control in their own respective way when saturation occurred in order to bring the system back into the linear region. The proposed new proportional integral controller consists of a separately controlled integration which functions to seek for steady state value with respect to the input command and stored the value throughout the system operation. Simulations done for unloading cases for induction motor plant and also a higher order transfer function plant. The results show that the proposed controller gives a promising performance when compared with the existing methods.

Keywords—component; Anti-windup; proportional-integral (PI) control; tracking back calculation; steady state.

I. INTRODUCTION

In variable speed motor drive, inner feedback loop is conducted by current control in order to obtain a fast dynamics and peak current protection, where its command is generated by the outer speed controller. The current command must be within a prescribed maximum value for the sake of preventing magnetic saturation, overheating of the motor and converter protection. However, in many cases such as sudden input change or when the motor encounter the disturbance or torque, the current command generated outboud the maxima range. For proportional integral derivative (PID) controller that works disregarding the input limit, windup phenomenon or saturation nonlinearity will occur in the speed control. The PID linear control will deteriorate when the saturation happened and motor performance exhibits large overshoot, longer time settling and even instability [1].

Although saturation problem has gained awareness among researchers, formal treatment on it only started from 1940s. As described in a review, researchers tried to investigate on the local stability or enforcing stability and the performance properties of the anti-windup compensator [1].

Reference [2] discussed the details about conditioning technique and emphasis on the modified set point and back calculation. Filtered setpoint was introduced to overcome the short-sightness problem and other conditioning drawbacks. In the paper [3], they suggested a Variable-Speed PID (VSPID) Control to prevent integrator windup. Even though they received comment from [4] which stated that a better comparison should be done between other techniques at their respective tuning parameter performance. Yet, the VSPID is claimed to be able to return the control law to linear operation faster compared to the anti-windup bumpless transfer (AWBT) technique [5].

Among the techniques, conditional integration, tracking back calculation and integral state prediction schemes have been commonly discussed among researchers. The conditional integration switches off the integral control and remains the same value when controller output saturation occurred. The integral controller will again back to duty after the saturation back to the linear region again. In the meanwhile, tracking back calculation scheme integrates the error by using the difference between the saturated and the unsaturated control signals to generate a feedback signal to properly control the integral state to ensure the integral state did not exceed the control limit [6]. In the paper, the PI plane is very useful in evaluating the anti-windup PI controller as it denotes the behaviour of the system based on the PI controller output path. From the PI plane also, it is noted that the overshoot and the setting time greatly depend on the touching condition rather than the PI gains. For any anti-windup scheme, the rise time is mainly related to the control input limit during the saturation range.

In this paper, a new proposed PI controller utilising steady state integral controller is suggested for variable speed motor drives. The approach independently controls the integral controller with an inner PI controller to ensure the integral to carry a steady state value with regards to the respective speed condition. The proposed minimises or prevents from integral windup phenomenon which is the concern of many, as the integral will seek and stay at the

steady state. The integral will not fluctuates as the normal integral control does, and will remain in steady state value or the current limiting value when falls into the saturation region. However, this is still in the fundamental stage and in this paper, only variable speed unload condition is considered and compared with conditional integration, tracking back calculation and integral state prediction schemes through simulation.

II. PI PLANE

PI plane is a plot of integral control against the proportional control value which indicates the controller trajectory at any instant of particular proportional and integral values. If the PI control output falls within the higher boundary, B_h and lower boundary, B_l lines, the controller is deemed to be in linear or unsaturated state and saturated if out bounded. As been discussed [7], the overshoot is influenced by the relative position of the integral state at the linear range boundary and the steady state value. The higher the integral state when touching the boundary compared to the steady state will apt to large overshoot. In the meanwhile, if the integral state is lower, a slow error response occurs. This is shown by the respective PI controller value trajectory in Fig. 1 where q_o and q_{ss} denote the linear range boundary integral error and steady state integral error respectively.

It is expected to have an overshoot whenever the trajectory moves into the negative $k_p e$ region given a positive input. For the $q_o \approx q_{ss}$, the trajectory becomes shorter to reach the steady state condition.

III. STEADY STATE INTEGRAL

By the state of the art of PI controller, the controller will have its tuning parameters that bring the system response into the desired input reference. The error obtained from the feedback signal reaches zero when the system attain the required output and the controller will also carry a steady state value as depicted below.

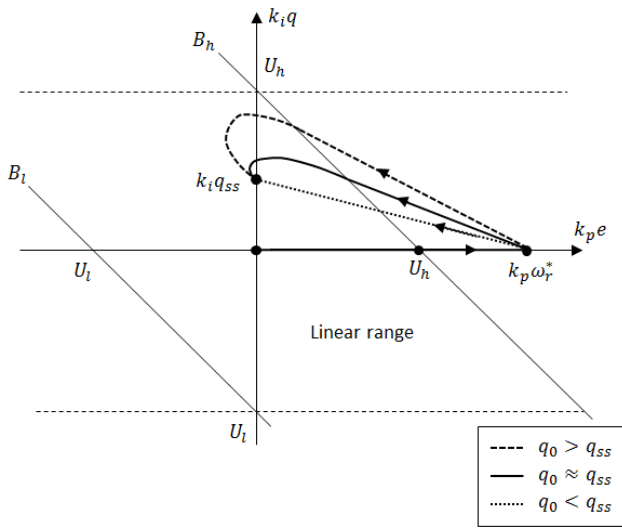


Figure 1. LINEAR PLANE AND ERROR TRAJECTORIES ON A PI PLANE.

A. Common Closed Loop System

Fig. 2 shows the common block diagram for a closed loop system with its controller and system plant. The system response transfer function is:

$$\begin{aligned} G(s) &= \text{Transfer function for plant} \\ k_p &= \text{Proportional tuning parameter} \\ k_i &= \text{Integral tuning parameter} \\ K(s) &= \text{Transfer function for PI controller} \\ Y_p(s) &= \text{Transfer function for plant output} \\ Y(s) &= \text{Transfer function for PI controller output} \\ U(S) &= \text{Transfer function for step input} \end{aligned}$$

$$Y_p(s) = \frac{U(s)K(s)G(s)}{I+K(s)G(s)} \quad (1)$$

The controller output signal can be obtained as follow.

$$Y(s) = \frac{U(s)K(s)}{I+K(s)G(s)} \quad (2)$$

Investigate the pi controller output value when reaching steady state for different plant transfer function types.

$$\text{Case 1: } G(s) = \frac{A}{Bs+C}$$

$$\frac{Y(s)}{U(s)} = \frac{(k_p s + k_i)(Bs+C)}{s(Bs+C) + (k_p s + k_i)A} \quad (3)$$

When steady state occurs:

$$\lim_{s \rightarrow 0} sY(s) = \frac{UC}{A} \quad (4)$$

$$\text{Case 2: } G(s) = \frac{As+B}{Cs^2+Ds+E}$$

$$\frac{Y(s)}{U(s)} = \frac{(k_p s + k_i)(Cs^2+Ds+E)}{s(Cs^2+Ds+E) + (k_p s + k_i)(As+B)} \quad (5)$$

When steady state occurs:

$$\lim_{s \rightarrow 0} sY(s) = \frac{UE}{B} \quad (6)$$

In general for any nth order transfer function plant:

$$G(s) = \frac{a_{m+1}s^m + a_m s^{m-1} + a_{m-1}s^{m-2} + \dots + a_1 s^0}{b_{n+1}s^n + b_n s^{n-1} + b_{n-1}s^{n-2} + \dots + b_1 s^0} \quad (7)$$

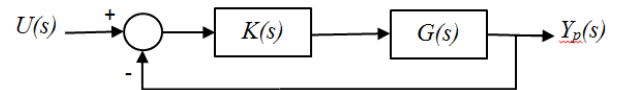


Figure 2. BLOCK DIAGRAM OF A CLOSED LOOP SYSTEM.

$$\lim_{s \rightarrow 0} sY(s) = \frac{U b_l}{a_1} \quad (8)$$

prediction and switched off as for conditional integration. The proposed back into the linear unsaturated region earlier than the rest.

VI. CONCLUSION

The proposed PI controller introduces another anti-windup controlling method. The separately sourced integral control closed loop incorporated with the modified tracking back calculation scheme ensure a steady state value of the respective input command be found and keep throughout the operation under unloading condition. Only the integral signal will be transferred into the system from the above closed loop and engaging with the P controller directed with the error, will make a PI controller. As compared with the existing methods, the proposed shows some promising results. The proposed reaches the steady state condition earlier and the integral control will not fluctuate as seen in the concerned methods. The method can be applied to higher order transfer function as well and this indicates the flexibility in implementing on other applications. However, this is still in the fundamental stage and test only been done on unloading cases. With the promising result, the proposed PI controller will be studied and expand to loading case and tested in hardware.

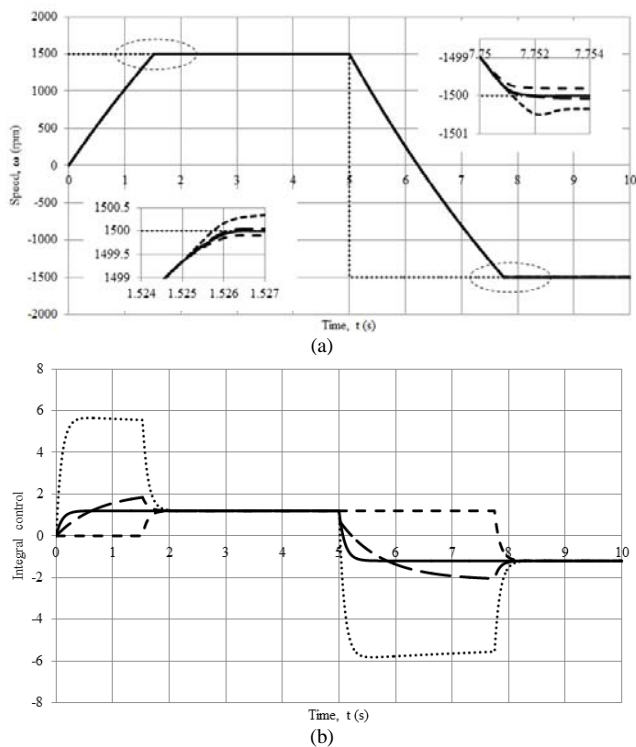


Figure 4. SIMULATED COMPARISON OF ANTI-WINDUP SCHEMES FOR FIRST ORDER PLANT SYSTEM WITH CHANGING STEP INPUT (SQUARE-DOTTED: TRACKING BACK CALCULATION, SHORT-DASH: CONDITIONAL INTEGRATION, LONG-DASH: INTEGRAL STATE PREDICTION, SOLID: PROPOSED SCHEME). (A). SPEED. (B). INTEGRAL CONTROL.

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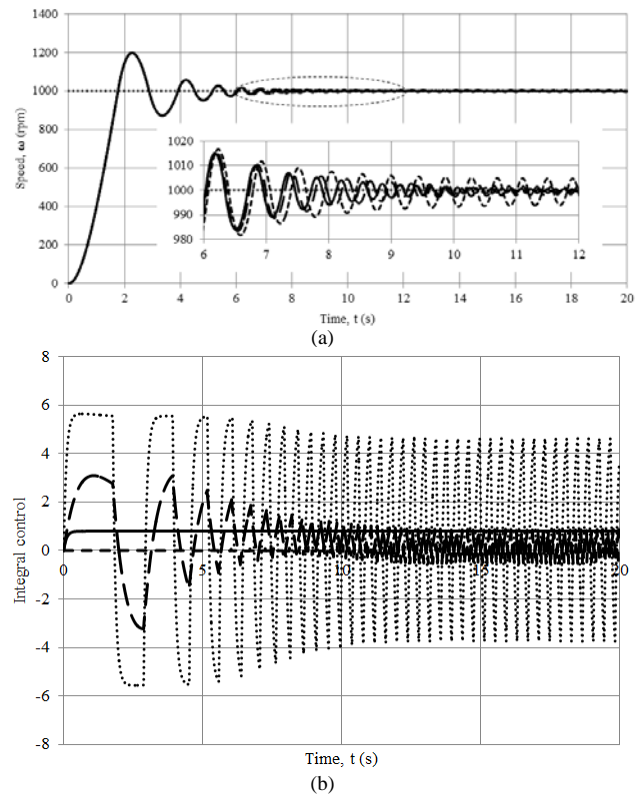


Figure 5. SIMULATED COMPARISON OF ANTI-WINDUP SCHEMES FOR SECOND ORDER PLANT SYSTEM WITH CONSTANT STEP INPUT (SQUARE-DOTTED: TRACKING BACK CALCULATION, SHORT-DASH: CONDITIONAL INTEGRATION, LONG-DASH: INTEGRAL STATE PREDICTION, SOLID: PROPOSED SCHEME). (A). SPEED. (B). INTEGRAL CONTROL.